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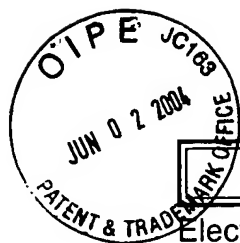
Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 61916
Application ID: 10091647
Title of Invention: Reset facility for redundant processor using a fibre channel loop
First Named Inventor: Aedan Coffey
Domestic/Foreign Application: Domestic Application
Filing Date: 2002-03-05
Effective Receipt Date: 2004-06-02
Submission Type: Information Disclosure Statement
Filing Type:
Confirmation number: 9151
Attorney Docket Number: ERLG.P-032




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TRANSMITTAL

Electronic Version v1.1
Stylesheet Version v1.1.0

Title of Invention	Reset facility for redundant processor using a fibre channel loop									
Application Number: 10/091647 										
Date: 2002-03-05										
First Named Applicant: Aedan Diarmuid Coffey										
Confirmation Number: 9151										
Attorney Docket Number: ERLG.P-032										
<p>I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.</p> <p>I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.</p>										
<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Carl Oppedahl Registered Number: 32746</td><td>/s/</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Carl Oppedahl Registered Number: 32746	/s/	Attorney		
Submitted by:	Elec. Sign.	Sign. Capacity								
Carl Oppedahl Registered Number: 32746	/s/	Attorney								
<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>erlgp032a-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr></table>			Documents being submitted	Files	us-ids	erlgp032a-usidst.xml		us-ids.dtd		us-ids.xsl
Documents being submitted	Files									
us-ids	erlgp032a-usidst.xml									
	us-ids.dtd									
	us-ids.xsl									
<p>Comments</p> <p>No fees are believed due with this submission as we have not received an action on</p>										

the merits. However, the Commissioner is authorized to debit any fees which may be due or credit any overpayments to Deposit Account No. 15-0610.



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Reset facility for redundant processor using a fibre channel loop						
Application Number:	10/091647						
Confirmation Number:	9151						
First Named Applicant:	Aedan Coffey						
Attorney Docket Number:	ERLG.P-032						
Art Unit:	2115						
Examiner:	Thomas C. Lee						
Search string:	(6658504 or 6188973 or 6115814 or 5956665 or 5892973 or 5790782 or 6061244 or 6050658 or 5183749 or 5814762 or 5790782 or 5483423 or 5313369).pn.						
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6658504	2003-12-02	Lieber et al.			
	2	6188973	2001-02-13	Martinez et al.			
	3	6115814	2000-09-05	Lieber et al.			
	4	5956665	1999-09-21	Martinez et al.			
	5	5892973	1999-04-06	Martinez et al.			
	6	5790782	1998-08-04	Martinez et al.			
	7	6061244	2000-05-09	O'Sullivan et al.			
	8	6050658	2000-04-18	O'Sullivan et al.			
	9	5183749	1993-02-02	Ramos et al.			
	10	5814762	1998-09-29	Tusler et al.			
	11	5790782	1998-08-04	Martinez et al.			
	12	5483423	1996-01-09	Lewis et al.			
	13	5313369	1994-05-17	Lewis et al.			
Signature							
Examiner Name					Date		

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